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## Issues on the molecular-beam epitaxial growth of *p*-SiGe inverted-modulation-doped structures

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The influence of boron segregation and silicon cap-layer thickness on two-dimensional hole gases (2-DHGs) has been investigated in Si/Si<sub>0.8</sub>Ge<sub>0.2</sub>/Si inverted-modulation-doped heterostructures grown by solid-source molecular-beam epitaxy. Boron segregation, which is significant in structures with small spacer layers, can be suppressed by growth interruption after the boron doping. How growth interruption affected the electrical properties of the 2-DHG and the boron doping profile as measured by secondary ion mass spectroscopy are reported. We report also on the role played by the unpassivated silicon cap, and compare carrier transport at the normal and inverted interfaces. © 1999 American Institute of Physics. [S0003-6951(99)04204-7]

The p-type Si/SiGe/Si modulation-doped (MD) structure is of interest in device physics and in high performance fieldeffect-transistor (FET) applications. A two-dimensional hole gas (2-DHG) can be formed at the bottom ("inverted") or top ("normal") interface of a Si/SiGe/Si quantum well, provided a Si boron-doped layer is grown at some distance before or after the alloy layer, respectively. The latter case refers to normal MD structures, which have been studied extensively.<sup>2,3</sup> There are fewer reports on the former arrangement, the so-called inverted MD structures that are more important in device applications. It is well known that in both two-dimensional electron and hole gas systems, the normal MD structures often have higher mobility than inverted ones. The inferior transport parameters of inverted structures have been addressed by considering dopant segregation<sup>4</sup> and interface roughness.<sup>5</sup> Moreover, the existence of surface charges, 6,7 due to surface states on the silicon cap, only affects the conductivity of the two-dimensional carrier gas in inverted structures due to the depletion of carriers in the well. This is avoided in the normal structure by providing an excess concentration of dopant in the doping supply layer. Usually another doped layer is grown near the cap surface of inverted structure to prevent this effect, 8 but it may cause a second 2-DHG at the normal interface. Here we report on how the proximity of the surface charges and boron segregation affect the 2-DHG at the inverted interface of Si<sub>0.8</sub>Ge<sub>0.2</sub> MD structures that have been grown by solid-source molecular-beam epitaxy (MBE).

A critical problem we have addressed is the surface accumulation of boron atoms during MBE growth,  $^{10-12}$  which can act as a reservoir source after closing the boron shutter. This can lead to smearing of the leading edge of the doping profile, a reduction or even possible elimination of the spacer and boron doping in the alloy. Boron incorporation during growth interruption was investigated by growing four boron doped layers of thickness 20 nm, concentration  $2 \times 10^{18} \, \mathrm{cm}^{-3}$  and separation 55 nm in silicon on a  $p^{++}$  (001) substrate at 575 °C and at a growth rate of 0.1 nm s<sup>-1</sup>. The

growth was interrupted for 80, 40, and 20 min, respectively, at 5 nm after closing the boron shutter, but there was no interruption after the final layer. The high resolution secondary ion mass spectroscopy (SIMS) depth profile for boron measured on this structure is shown in Fig. 1. It can be seen that narrow boron spikes (labeled with arrows) have been formed exactly where the growth has been interrupted, indicating the surface accumulated boron atoms, and the sheet density in the spike is higher the longer the interruption. The total measured sheet concentration of boron is the same in each doping layer  $(3.5\pm0.1\times10^{12}\,\mathrm{cm}^{-2})$ , precluding external sources of boron contributing to the narrow spikes observed at the interruptions. The profile also reveals that the integrated sheet density of boron atoms that segregate to dope the pseudo spacer layer after each doping spike/growth interruption, decreases with growth interruption time. These observations suggest that a surface accumulation layer of approximately  $1 \times 10^{12}$  cm<sup>-2</sup> of boron atoms forms in each case

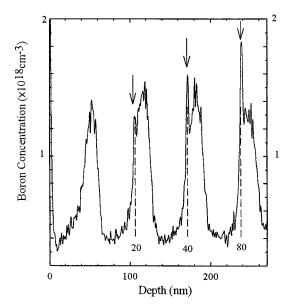


FIG. 1. Boron concentration profile of four 20 nm Si boron doped layers separated by 55 nm Si. Growth was interrupted for 80, 40, and 20 min at 5 nm after the first, second, and third doped layers, as shown. (Note the linear B concentration scale.)

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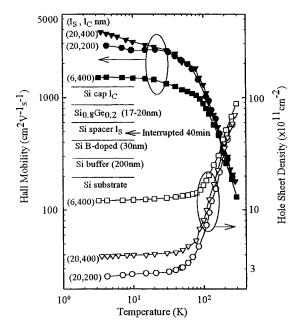


FIG. 2. Hall mobility (filled symbols) and carrier density (hollow) vs temperature for three inverted structures with different spacer ( $l_S$ ) and cap ( $l_C$ ) thickness:  $l_S$ =20 nm and  $l_C$ =200 nm (circles),  $l_S$ =20 nm and  $l_C$ =400 nm (triangles), and  $l_S$ =6 nm and  $l_C$ =400 nm (squares).

during the boron doping, but that this is increasingly "stabilized" by growth interruption, so that only a fraction of it segregates to dope subsequently deposited material. The origin of this stabilization phenomenon could be the C or O containing impurities that have accumulated on the surface during interruption 13—species which are known to inhibit boron diffusion, or due to some reconstruction of the accumulated boron, including clustering. In subsequent experiments we adopted a 40 min growth interruption 3–5 nm after growing doped layer as a compromise practical solution to minimize boron segregation.

The inverted MD structures were grown on 10  $\Omega$  cm n-type Si (001) substrate and consisted of the following layer sequence: 200 nm Si buffer grown at 700°C, 30 nm 2  $\times 10^{18}$  cm<sup>-3</sup> Si:boron doping layer grown at 575 °C, 6–20 nm Si spacer (dopant setback) grown at 575 °C, 17-20 nm Si<sub>0.8</sub>Ge<sub>0.2</sub> layer grown at 610 °C, and finally a Si layer of 150 or 400 nm thickness (see inset to Fig. 2). Normal structures were also grown for comparison, in which the growth temperature was reduced to 610 °C before growing the alloy, spacer and Si:boron capping layers. Contacts to the 2-DHG were made by Al sputtering and annealing at 470-520 °C in a nitrogen atmosphere. Standard Hall bars and Van der Pauw cross shaped devices were prepared by Si wet etching. Resistivity and Hall effect measurements were carried out for a 500 nA ac current using the lock-in amplifier method at temperatures down to 1.6 K.

The details and measured transport parameters at 4.2 K of the normal and inverted (denoted by N and I, respectively) structures, are listed in Table I, and the temperature dependence of the mobility and hole sheet density of three inverted structures is given in Fig. 2. As can be seen, all the samples (including normal structures) had a near constant carrier sheet density  $n_s$  at  $T \le 40$  K, indicating a 2-DHG at the respective SiGe/Si interface. The inverted structure with a 6 nm spacer layer and 400 nm capping layer. (sample B) has

TABLE I. The structural and measured (4.2 K) transport parameters of some of the normal (N) and inverted (I) structures under study.

| Sample I.D. | Spacer $l_S$ (nm) | $\begin{array}{c} \text{Cap} \\ l_C \\ (\text{nm}) \end{array}$ | Hole<br>Mobility<br>cm <sup>2</sup> V <sup>-1</sup> s <sup>-1</sup> | 2-DHG<br>Density<br>$\times 10^{11}  \mathrm{cm}^{-2}$ |
|-------------|-------------------|---|---|--|
| A(N)        | 4                 |   | $1950 \pm 100$  | $8.9 \pm 0.2$  |
| B(I)        | 6                 | 400   | $1550 \pm 100$  | $12.2 \pm 0.2$   |
| C(N)        | 6                 |   | $2500 \pm 100$  | $7.2 \pm 0.2$  |
| D(N)        | 20                |   | $4330 \pm 100$  | $4.3 \pm 0.2$  |
| E(I)        | 20                | 400   | $3700 \pm 100$  | $4.0 \pm 0.2$  |
| E' (etched) | 20                | 200   | 2870  | 2.5  |
| F(I)        | 20                | 150   | $2600 \pm 400$  | $2.6 \pm 0.5$  |
| F' (gated)  | 20                | 150   | 4550(max)   | 5.2(max)   |

higher carrier density in comparison to that of the normal ones (samples A and C), indicating a reduction of the effective spacer thickness due to the residual boron segregation into the spacer.<sup>4</sup>

Thicker spacer layers (20 nm) were used in the other samples giving correspondingly lower  $n_s$  value. However, considering sample E, Poisson-Schrödinger calculations indicate that a higher  $n_s$  ( $\sim 5 \times 10^{11}$  cm<sup>-2</sup>) than that observed  $(4 \times 10^{11} \,\mathrm{cm}^{-2})$  should be obtained with these sample parameters. This reduction in  $n_s$  can be attributed to the proximity of the unpassivated surface of the Si cap, where donortype surface states trap holes from the 2-DHG in the SiGe. This was confirmed in sample F where the thinner Si cap produced a further reduction in  $n_s$ . Further confirmation was obtained by reducing the cap thickness of sample E using a slow (2.5 °A/s) Si etchant to 200 nm, again significantly reducing the  $n_s$  value. This influence of the Si cap surface could be removed either by passivating the cap surface or by forward biasing a Schottky gate formed on the Si cap. The latter was achieved  $^{14}$  in sample F' where a Ti/Al gate was sputtered (onto sample F) and forward biased to give a  $n_s$ value  $(5.2 \times 10^{11} \, \text{cm}^{-2})$  close to the theoretical value in the absence of Si cap influences.

Figure 3 shows how the 2-DHG Hall mobility  $\mu$  varies with  $n_s$  for all the samples used in this study, including the grown, etched, and gated samples. The apparent universal relationship between  $\mu$  and  $n_s$ , irrespective of which interface (N or I) is involved, the cap thickness, and whether the cap surface is unpassivated or gated, strongly suggests that the 2-DHG carrier mobilities at either SiGe/Si interface are limited by similar short range scattering processes. It can therefore be tentatively concluded that there are not large differences between interface roughness, interface charge, and/or alloy scattering at inverted and normal interfaces (all these processes playing a role at 4.2 K). The parameters studied merely alter the  $n_s$  value and do not contribute to carrier scattering.

In conclusion, the effects of boron segregation and cap layer on 2-DHG transport properties have been investigated in Si/Si<sub>0.8</sub>Ge<sub>0.2</sub>/Si inverted-modulation-doped structures grown by solid-source MBE. Boron segregation can be suppressed to an extent by interruption growth after completion of boron doping, by a process possibly linked to the C and O impurities which accumulate on the surface during growth interruptions. The unpassivated Si cap surface traps holes

nm spacer layer and 400 nm capping layer, (sample B) has interruptions. The unpassivated Si cap surface traps hole Downloaded 14 Jul 2009 to 137.205.202.8. Redistribution subject to AIP license or copyright; see http://apl.aip.org/apl/copyright.jsp

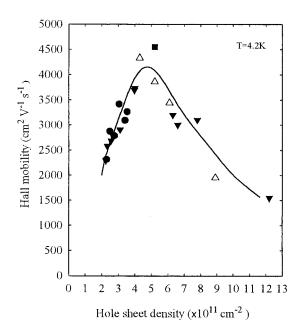


FIG. 3. Variations of the Hall mobility (at 4.2 K), vs hole sheet density for all the inverted (filled symbols) and normal (hollow) structures used in this study including: the grown (triangular), etched (circle), and gated (square) samples. The solid line provides a guide for the eye.

from the inverted interface to an extent depending on proximity to it. The study suggests that while carrier sheet density at inverted structures is influenced by boron segregation and cap surface states, the carrier mobility is similar to that of the normal interface and limited by short range scattering processes

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